



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

OHKAWA, Narumi

Group Art Unit: 2823

Serial No.: 09/961,264

Examiner: DANG, Trung Q.

Filed: September 25, 2001

P.T.O. Confirmation No.: 7825

For: SEMICONDUCTOR DEVICE HAVING BOTH MEMORY AND LOGIC CIRCUIT
AND ITS MANUFACTURE

TECHNOLOGY CENTER 2800
JAN 14 2003
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Commissioner for Patents
Washington, D.C. 20231

Date: January 13, 2003

Sir:

This paper is submitted in response to the Official Action dated December 20, 2002.

In the Action, restriction is required between Group (I), Claims 1-4, 7-10, 13 and 15; and Group (II), Claims 5-6, 11-12, and 14.

Applicant hereby elects the subject matter of Group (I), Claims 1-4, 7-10, 13 and 15 for prosecution in this application.

It should be noted that **claims 5-6, 11-12, and 14 have already been canceled** in item 17b of Application Transmittal. This application is a **division of prior application** Serial No. 09/288,302 filed on April 8, 1999.

This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

#6
Election
1-16-03
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Serial No. 09/961,264

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



Sadao Kinashi
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Reg. No. 48,075

SK/fs

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